# Islamic Azad University
### Tehran South Branch

Date: March 9, 2012
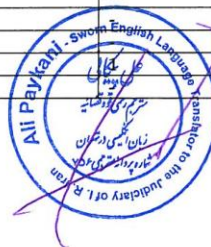No. 08-14-6-04/1483

## Academic Transcript

Name and Surname: **Ms. Mansoureh Labafniya**
Father's Name: Morteza
Birth Certificate No. 12598, issued at Tehran
D.O.B. 1986
Field of Study: **Computer Engineering (Hardware)**
Degree: **Bachelor's Degree**
Date of Admission: 1st Semester, 2004-05
Date of Graduation: July 22, 2008

| Course Title | Type | Credit | | Grade | Grade Points |
|---|---|---|---|---|---|
| | | Theoretical | Practical | | |
| **1st Semester, 2004-05** | | | | | |
| Fundamentals of Computer and Programming | Core | 4 | - | 19.75 | 79 |
| Physics I | Basic | 3 | - | 11 | 33 |
| Islamic Texts (Arabic Learning) | General | 2 | - | 16.5 | 33 |
| Farsi Language | General | 3 | - | 18 | 54 |
| General Mathematics I | Basic | 3 | - | 17 | 51 |
| **2nd Semester, 2004-05** | | | | | |
| Discrete Structures | Core | 3 | - | 20 | 60 |
| Islamic Education I | General | 2 | - | 17 | 34 |
| Physics II | Basic | 3 | - | 16 | 48 |
| Physics I Lab. | Basic | - | 1 | 16.5 | 16.5 |
| General Mathematics II | Basic | 3 | - | 13 | 39 |
| Advanced Programming | Core | 3 | - | 17 | 51 |
| Population and Family Planning | General | 2 | - | 18 | 36 |
| **1st Semester, 2005-06** | | | | | |
| Electric Circuits I | Core | 3 | - | 19 | 57 |
| Physical Education I | General | - | 1 | 20 | 20 |
| Differential Equations | Basic | 3 | - | 11.5 | 34.5 |
| Engineering Statistics and Probability | Basic | 3 | - | 17.25 | 51.75 |
| Physics II Lab. | Basic | - | 1 | 17.5 | 17.5 |
| English Language | General | 3 | - | 18.75 | 56.25 |
| Machine Language and System Programming | Core | 3 | - | 14 | 42 |
| Data Structure | Core | 3 | - | 16 | 48 |
| **2nd Semester, 2005-06** | | | | | |
| Electric Circuits I Lab. | Core | - | 1 | 14.5 | 14.5 |
| Logic Circuits | Core | 3 | - | 11.5 | 34.5 |
| History of Islam | General | 2 | - | 20 | 40 |
| Physical Education II | General | - | 1 | 18.5 | 18.5 |
| Electronic Circuits | Core | 3 | - | 14 | 42 |
| English Language for Computer Engineering | Core | 2 | - | 17 | 34 |
| Algorithms Design | Core | 3 | - | 20 | 60 |
| Computer Lab. | Core | - | | 19.5 | 19.5 |
| Electric Circuits II | Major | 3 | | 15 | 45 |

علی پیکانی  Paykani Ali

مترجم رسمی قوه قضائیه جمهوری اسلامی ایران
در زبانهای انگلیسی (پروانه ۷۵۶) و فرانسه (پروانه ۹۶۸)
تهران، میدان انقلاب، بین خیابان ۱۲ فروردین و اردیبهشت
روبروی سینما بهمن، پاساژ اندیشه، طبقه زیر همکف، واحد ۵
تلفن: ۰۹۱۲۳۳۹۶۳۶۸-۶۶۴۶۴۰۴۵

Official translator & interpreter of English & French to the Judiciary of Iran
Traducteur assermenté du français et de l'anglais près le Pouvoir judiciaire

شمارهٔ  ۹۸۷۱۸۷  سری غ

ردیف دفتر ثبت

جمهوری اسلامی ایران

قوهٔ قضائیه ـ اداره مترجمین رسمی

| 1st Semester, 2006-07 | | | | | |
|---|---|---|---|---|---|
| Numerical Computations Methods | Elective | 3 | - | 15 | 45 |
| Logic Circuits Lab. | Core | - | 1 | 20 | 20 |
| Theory of Languages and Automata | Core | 3 | - | 14.5 | 43.5 |
| Artificial Intelligence | Elective | 3 | - | 17.5 | 52.5 |
| Engineering Mathematics | Core | 3 | - | 16 | 48 |
| Islamic Ethics and Education | General | 2 | - | 18 | 36 |
| Electronic Circuits Lab. | Core | - | 1 | 15 | 15 |
| Computer Architecture | Core | 3 | - | 18.5 | 55.5 |
| | | | | | |
| 2nd Semester, 2006-07 | | | | | |
| Training | Training | - | 2 | 19 | 38 |
| Microprocessor I | Core | 3 | - | 17.5 | 52.5 |
| Scientific and Technical Subjects Presentation | Core | 2 | - | 12 | 24 |
| Computer Architecture Lab. | Core | - | 1 | 19.25 | 19.25 |
| Digital Electronics | Major | 3 | - | 17.75 | 53.25 |
| Programming Languages Design and Implementation | Core | 3 | - | 16 | 48 |
| Operating Systems | Core | 3 | - | 12.75 | 38.25 |
| Digital Circuits Automatic Design | Elective | 3 | - | 20 | 60 |
| | | | | | |
| 1st Semester, 2007-08 | | | | | |
| Microprocessor II | Elective | 3 | - | 18 | 54 |
| The Holy Quran | General | 1 | - | 16.5 | 16.5 |
| Digital Electronics Lab. | Major | - | 1 | 16.5 | 16.5 |
| VLSI Systems Design | Major | 3 | - | 19.5 | 58.5 |
| Computer Networks | Core | 3 | - | 17.5 | 52.5 |
| | | | | | |
| 2nd Semester, 2007-08 | | | | | |
| Interface Circuits Design | Elective | 3 | - | 17.5 | 52.5 |
| General Workshop | Basic | - | 1 | 15 | 15 |
| Islamic Education II | General | 2 | - | 13 | 26 |
| The Islamic Revolution of Iran and its Origins | General | 2 | - | 16 | 32 |
| Imam Khomeini's Testament | General | 1 | - | 13 | 13 |
| Data Transfer | Major | 3 | - | 13 | 39 |
| Signals and Systems | Major | 3 | - | 16.5 | 49.5 |
| Project | Project | - | 3 | 20 | 60 |
| Microprocessor I Lab. | Core | - | 1 | 20 | 20 |
| Computer Networks Lab. | Major | - | 1 | 20 | 20 |
| Linear Control Systems | Major | 3 | - | 15.5 | 46.5 |

Total Credits Passed: 144     Total Credits Taken: 144     G.P.A.: 16.60 (out of 20)

Note:
- In the education system of university in each semester: 1 Theo. Credit = 17 hours, 1 Prac. Credit = 34 hours, 1 credit of externship = 51 hours, 1 credit of internship = 68 hours.
- Evaluation standard of course is from 0 to 20 and the minimum passing grade in associate and bachelor degree is 10 and in master degree 12 and in specialty doctorate is 14.

This transcript is valid without alteration and registered and confirmed under No. 90038696 dated March 15, 2012 in the Graduates Affairs Department of the University.
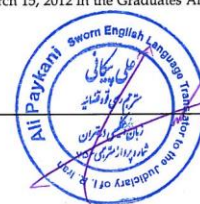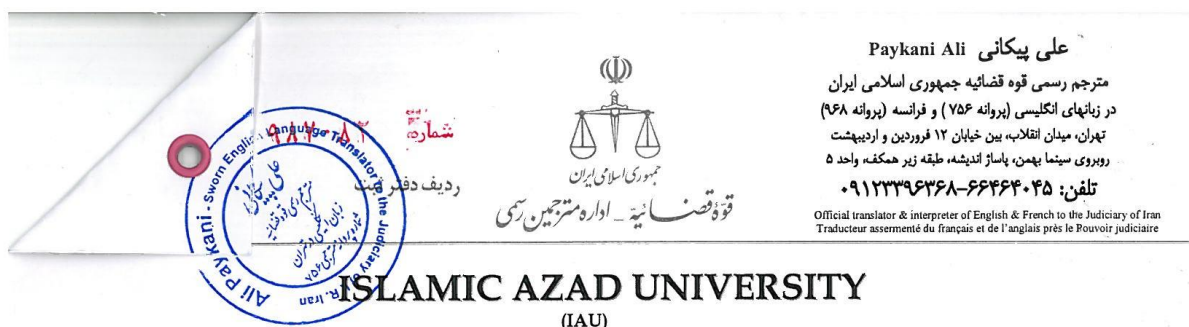
Seale and signed:
Deputy of Students Dept. of the University Branch
Dean of the University Branch
Director General of the Graduates Department

Translation is accurate and translator is competent to translate. Tehran, Oct. 4, 2014 (M.Es.4415)

Paykani Ali علی پیکانی

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قوة قضائیة ــ اداره مترجمین رسمی

# ISLAMIC AZAD UNIVERSITY
## (IAU)

Sealed Photo of
the Holder

Serial No. 0804046

## Bachelor's Degree

*Pursuant to the university charter passed by Supreme Council of Cultural Revolution on Nov. 3, 1987 and single article passed by Islamic Consultative Assembly (Iranian Parliament) on May 4, 1988,*

Whereas, **Ms. Mansoureh Labafniya**, daughter of Morteza, Birth Certificate No. 12598 issued in Tehran, born in 1986, successfully met all the requirements of **Bachelor's Degree** (Full-time) in **Computer Engineering (Hardware)**, at Tehran South Branch of Islamic Azad University on July 22, 2008, passing 144 credits and gaining **G.P.A. of 16.60** (out of 20), this diploma is awarded to her.

Signed
Dean of the University Branch: Dr. Mahmoudreza Esfandiar
President of Islamic Azad University: Dr. Abdollah Jasbi

Administration Reg. No.: 168714101893
Administration Date of Reg.: April 11, 2009

Translation is accurate and translator is competent to translate. Tehran, Oct. 4, 2014 (M.Es.4415)